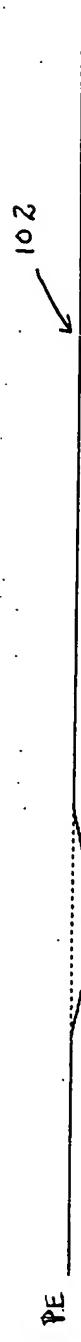


208730 " THREE EIGHTS

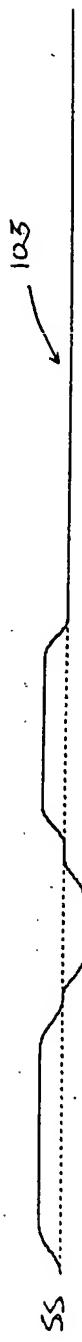
X: 50.0000 MSec., 387 PmLocs, Ranges: DAC {86, 5012}, Volts {0.0131, 0.7648}, Amps {0.6561, 38.2397}



Y: 50.0000 MSec., 369 PmLocs, Ranges: DAC {-4774, 80}, Volts {-0.7285, 0.0122}, Amps {-36.4238, 0.6104}



Z: 60.0000 MSec., 401 PmLocs, Ranges: DAC {5542, 6505}, Volts {0.8457, 1.0048}, Amps {42.2834, 50.2411}



Sh: 50.0000 MSec., 200 PmLocs, Ranges: DAC {330, 1842}, Volts {0.9282, 4.4393}, Amps {0.0000, 0.0000}

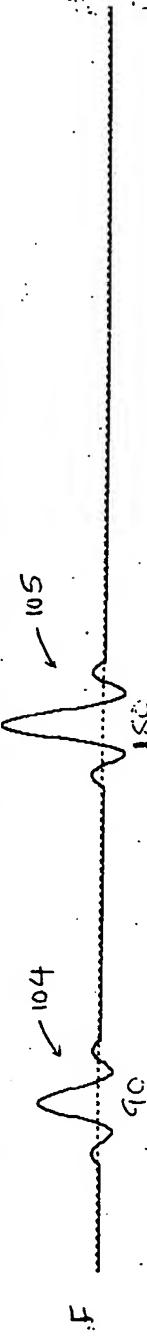


FIG 1a. STANDARD SPIN ECHO SEQUENCE (PRIOR ART)

2018-07-20 "THERMISTOR TEST"

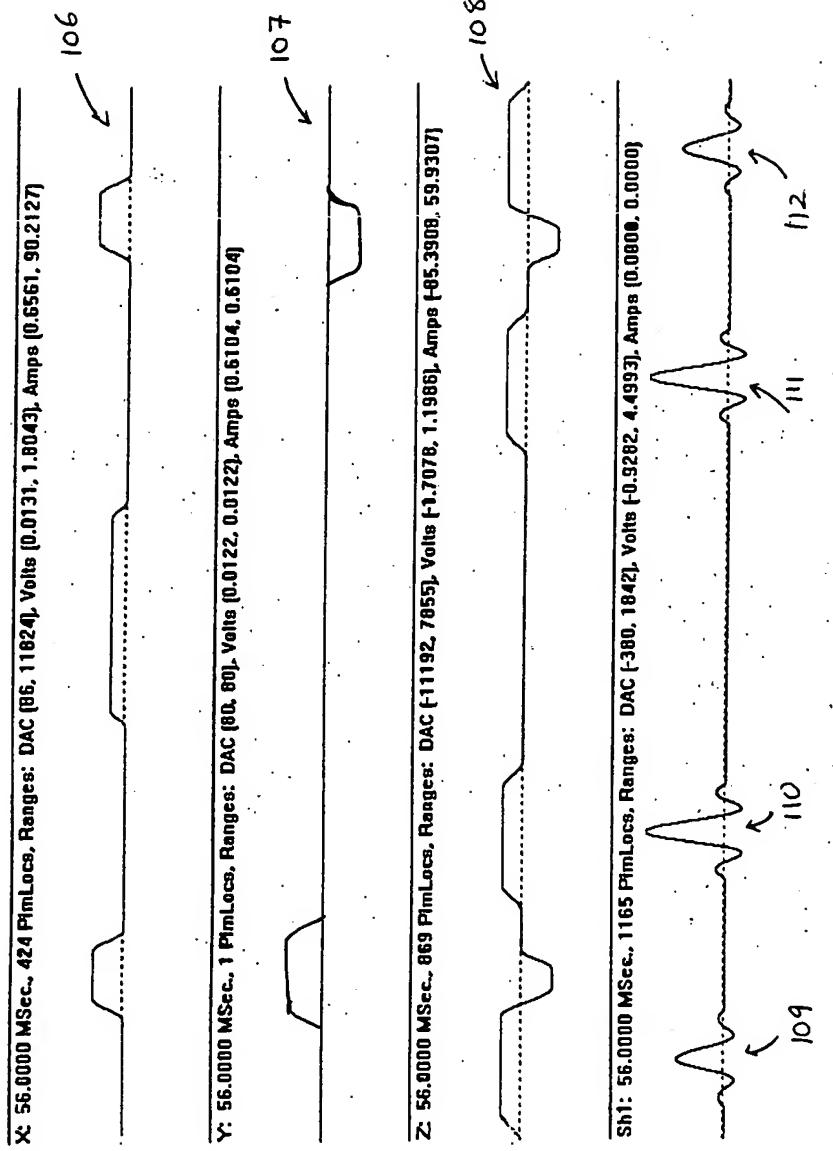


Fig 1b. STANDARD SPIN ECHO DRIVEN EQUILIBRIUM. (PRIOR ART)

208130" THREE EIGHT

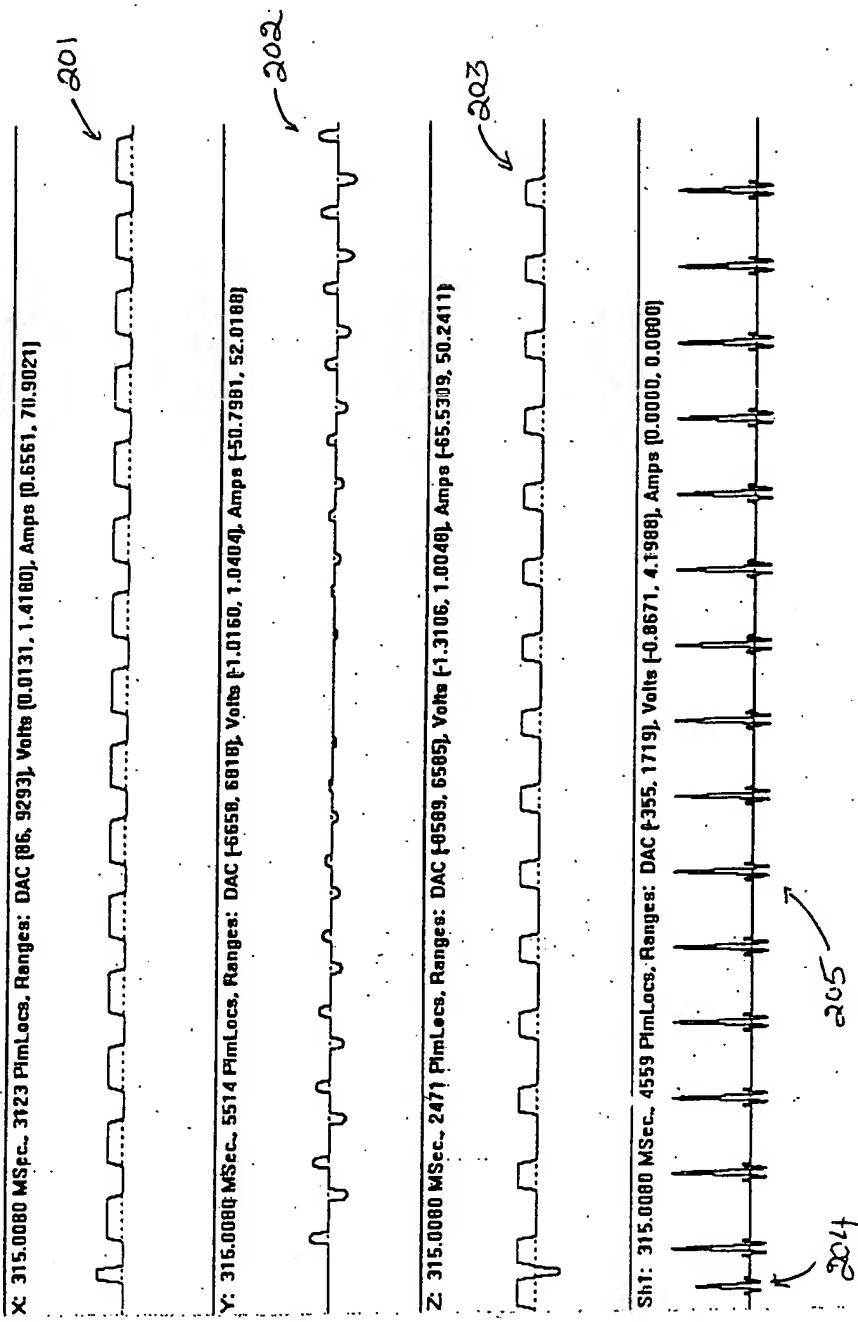


FIG 2 . FAST SPIN ECHO SEQUENCE

203130 " 444250 037

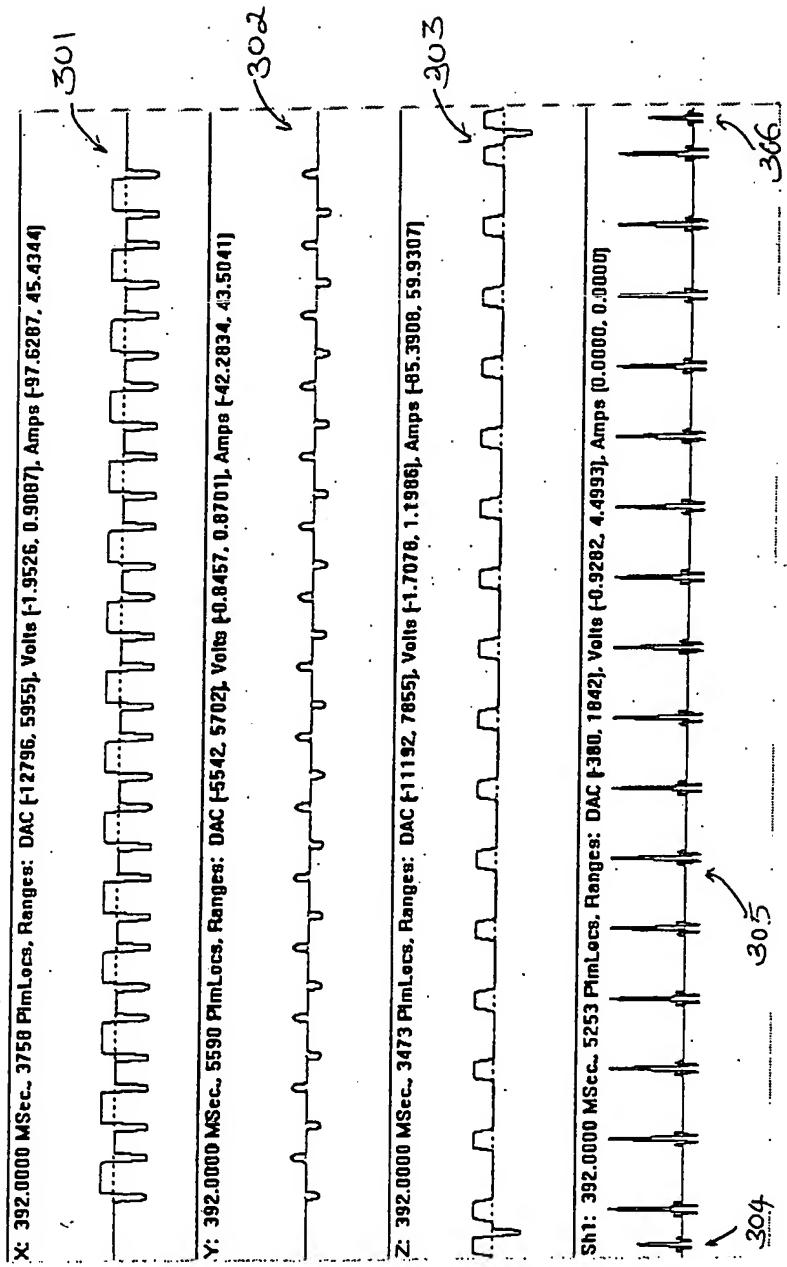


FIG 3. FAST SPIN ECHO combined with DENSE EQUILIBRIUM.